

**PATENT NUMBER**

MT	O.I.P.E.	PATENT DATE
SCANNED 1052	Q.A. 50	

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Method and apparatus for supporting a circuit component having solder column interconnects using external support

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims Print Claim for O.G.
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